

Fig. 1

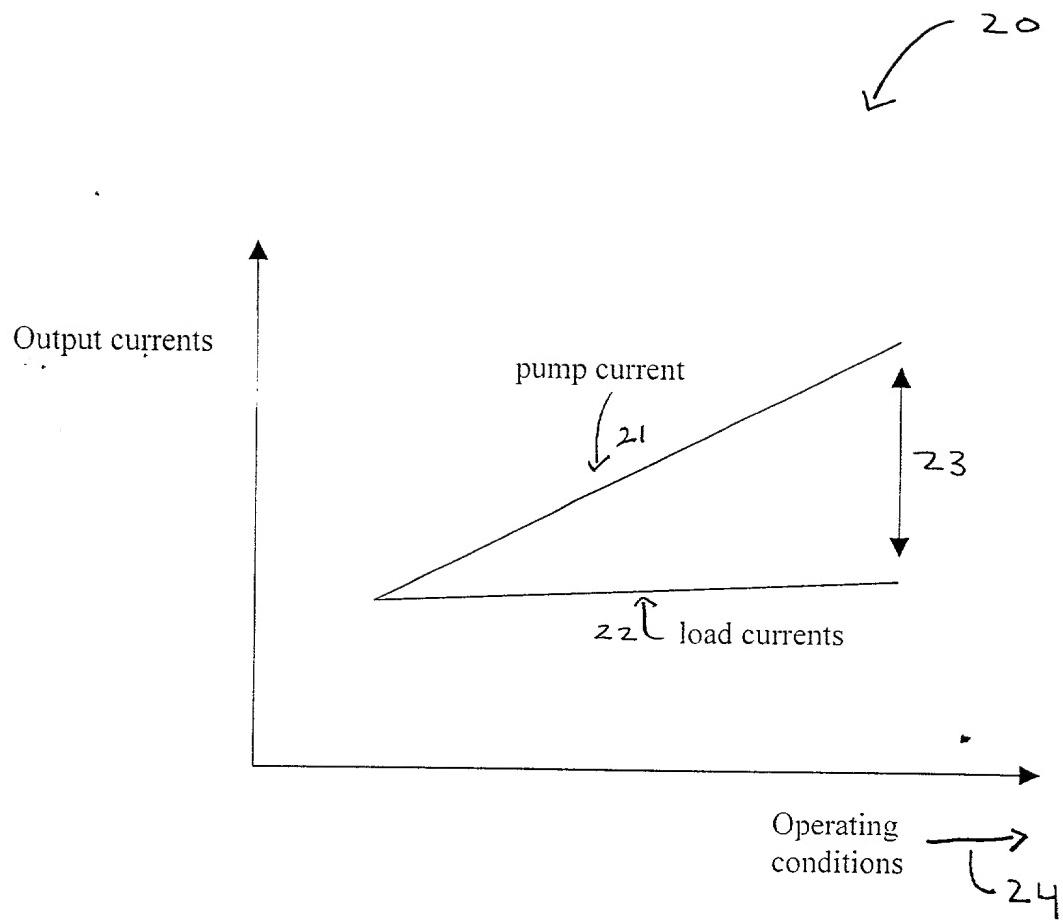


FIG. 2

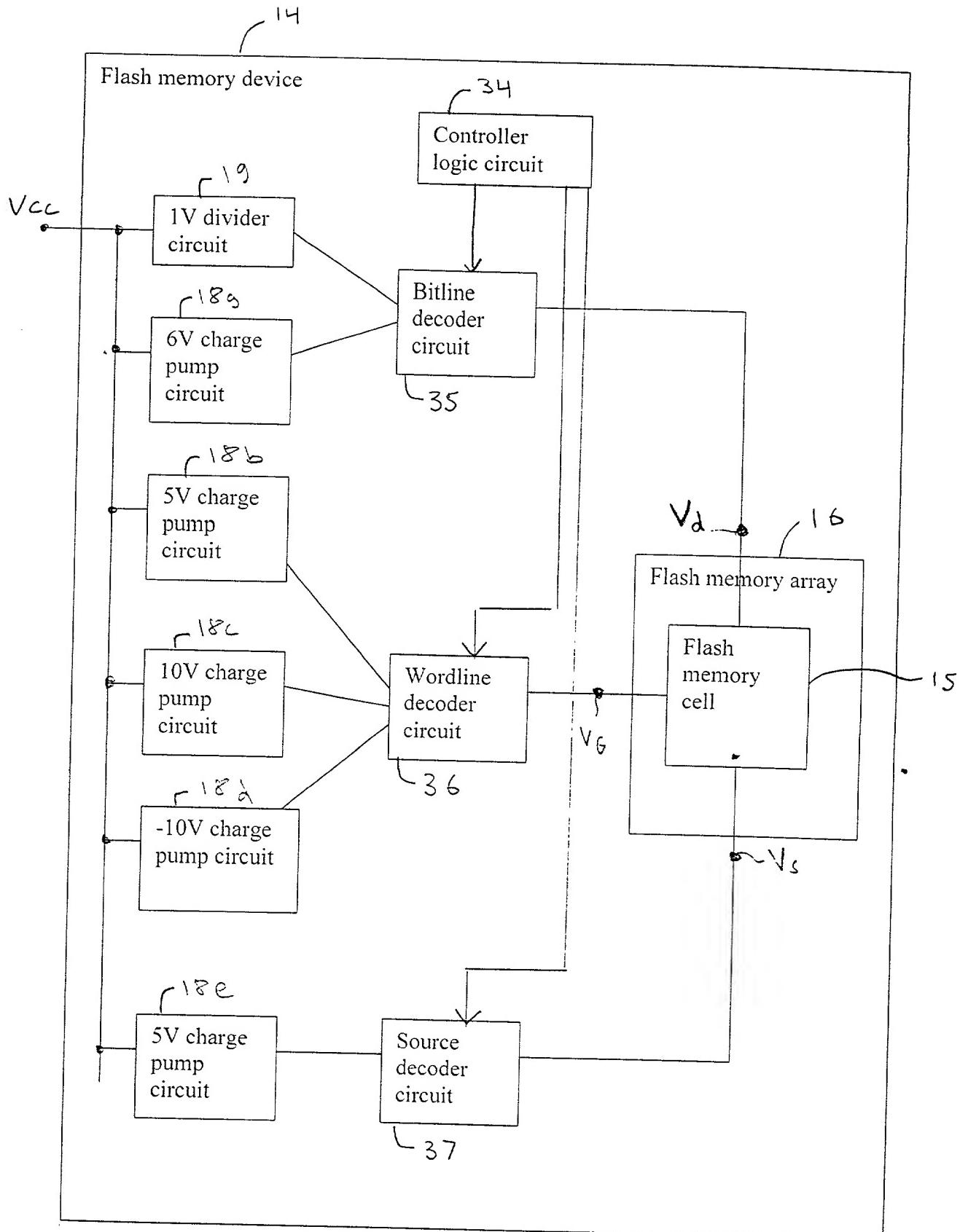


FIG. 3A

40

Table : Voltages For Processing Flash Device

	Vg	Vd	Vs
Program	10	6	0
Erase	-10	0	float
Read	5	1	0

Fig. 3B

18a

Charge pump circuit

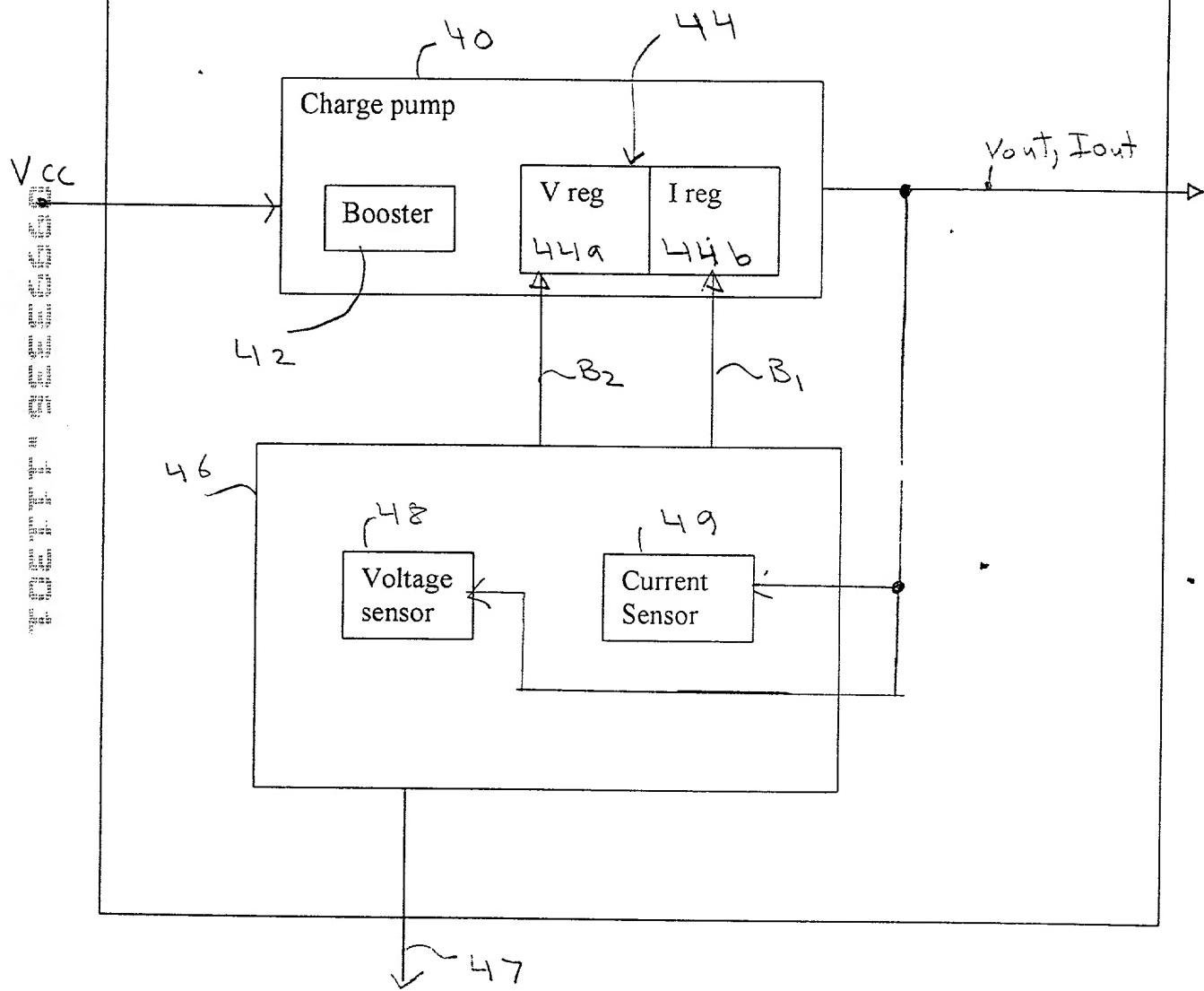


FIG. 4

18a

Charge pump circuit

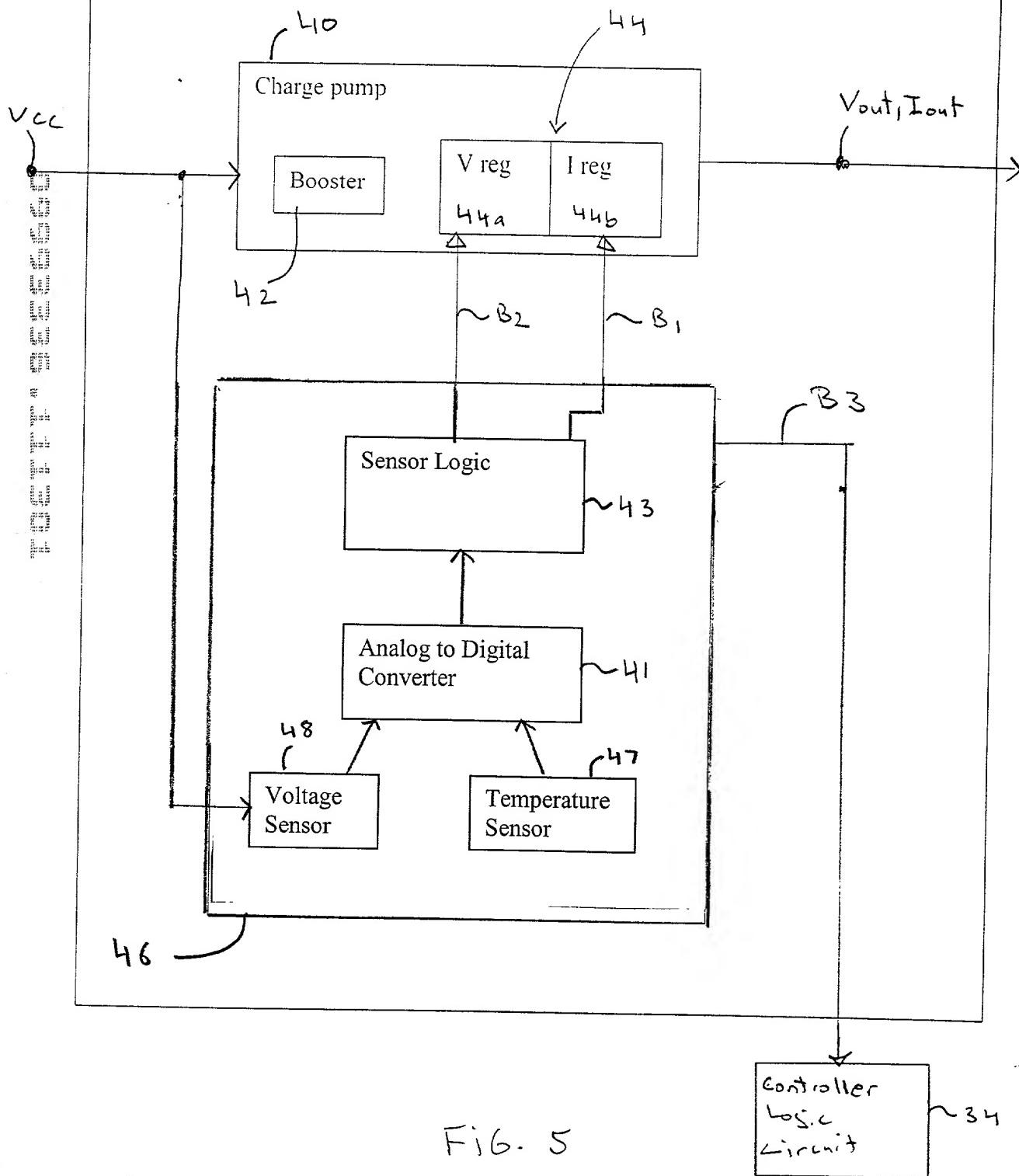


Fig. 5

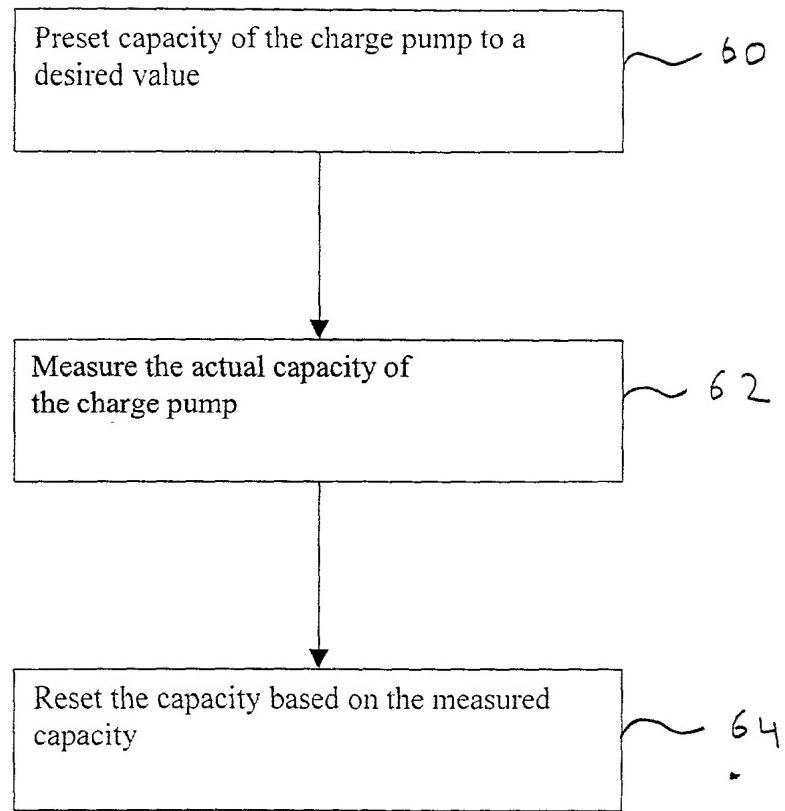


FIG. 6